



Job Description

FPGA DESIGN ENGINEER

Exciting opportunity with a Cambridge based designer & manufacturer of computer connectivity hardware.

OVERVIEW

Due to the success of our leading edge IP-based hardware solutions and rapidly expanding customer demand for enhanced functionality, we are seeking an FPGA Design Engineer who has wide ranging experience with Hardware Design, FPGA Design and Microprocessors to work on our innovative next generation hardware solutions. The successful candidate will play a key role in our new technology development team working on custom RTL for high performance KVM solutions.

KNOWLEDGE & EXPERIENCE

Essential

- Expert knowledge of FPGA design in VHDL and Verilog
- Extensive experience in FPGA design, implementation, and verification
- Proficiency with both Xilinx (ISE & Vivado) and Altera tools and Quartus
- Experience with ModelSim for simulation
- Demonstrable success and performance in FPGA design
- Strong problem solving and debugging abilities
- Ability to work well with all types of people, contributing on team level and as well as on an individual level

Desirable / useful

- System Verilog for simulation (UVM/OVM)
- Display Port and/or HDMI experience
- Experience working with USB2 and USB3



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- Experience working with video and audio
- FPGA connectivity through PCI Express, 1G/10G Ethernet, high-speed serial links
- Good understanding of software development, especially low level device drivers, network, multi-threaded programming

General

- Excellent verbal and written communication skills
- Ability to produce optimised and innovative solutions
- A passion for delivering a quality solution
- A good degree or equivalent educational qualifications in an appropriate discipline