



Job Description

HEAD OF HARDWARE ENGINEERING

Exciting opportunity with a Cambridge based designer & manufacturer of computer connectivity hardware.

PURPOSE OF ROLE

The Head of Hardware Engineering is one of the key members of the Engineering Management team of Adder Technology. They lead and manage the team responsible for design and development of all aspects of the hardware design of Adder's world leading products.

This position requires someone who possesses good leadership skills in addition to strong technical skills. As a leader and manager, the Head of Hardware Engineering is responsible for ensuring that all project work is scheduled and completed on time, within budget, and to Adder's demanding quality standards. The role offers an opportunity to be both lead and be hands on in the design process acting as architect, engineer, and designer for some key projects. This allows the Head of Hardware Engineering to lead by example and pilot cutting edge approaches.

As an expert technical contributor, the Head of Hardware Engineering is accountable for driving innovation and invention, overseeing all technical work, setting and maintaining design standards, introducing and incorporating the latest design methods and tools, improving internal and external design practices, and keeping our envied reputation for reliability and technology at the forefront of the industry.

This is an ideal position for an accomplished Senior Manager who is ready to make a difference and continuously create innovative and exciting new products that are critical to our customers' success. If you love Hardware Engineering, want to work in a team that is focused on becoming world class and in a company that is constantly profitable, combining the best of being privately owned and being part of the high tech Cambridge phenomenon, we want to talk with you!



Job Description

EDUCATION AND EXPERIENCE

Technical

- A strong degree in Electronics or an equivalent subject.
- Minimum of 7 years of experience in product design as an Electronic Engineer.
- Minimum of 3 years of experience managing hardware teams of up to seven persons.
- Experience creating cost effective solutions that meet aggressive product feature, cost, quality, performance, and time to market requirements.
- Experience supporting complete product solutions from concept to grave.
- Experience of high speed digital design and development methodologies.
- Experience of analogue design and development methodologies.
- Knowledge of FPGA design in VHDL and Verilog and associated development methodologies.
- Experience of schematic capture tools.
- Experience of layout tools.
- Experience of debugging using high speed scopes and other tools.

General

- Superior problem solving skills: issue identification, problem structuring, analysis, and development of recommendations.
- Focus and emphasis on teamwork to achieve results.
- Excellent interpersonal skills.
- Strong leadership skills.
- Good English language communication skills, both written and oral.
- Ability to work well with all types of people, contributing on team level and as well as on an individual level.



Job Description

DUTIES AND RESPONSIBILITIES

- Lead all design and development activities within the Hardware Engineering team located at the Bar Hill site in Cambridge.
- Manage a small team to get the most out of them, delivering projects on time and to a high quality.
- Schedule work and activities for all department staff. Plan top-down design schedules and resource plans with cross-functional product development team to meet project timescales.
- Oversee recruiting and hiring activities for all the hardware team.
- Develop, maintain, and champion tool chains and internal processes for product development.
- Develop and maintain internal documentation (SharePoint sites etc.).
- Utilize highly effective interpersonal skills to build and grow relationships with both internal team members and third party providers.